

**Amendments to the Specification**

Please replace the paragraph that starts on page 11, line 17, with the following amended paragraph:

Fig. 9 shows an embodiment of a V4 line according to the present invention. A column of a 2-dimensional array is shown that includes six rows of LABs. In this embodiment, the V4 line allows vertical unidirectional communication over a distance covering four function blocks. A V4 line 750 has a starting point at a row (N+5) and an ending point at a row (N+1). The V4 line 750 can connect to LABs 733-742. A multiplexer 759 selects one of the inputs as its output and this output is driven on the V4 line 750 by a driver 761. The V4 line 750 is driven once at its starting point and thus is an unidirectional line. In Fig. [[6]] 9, the V4 line 750 is driven upstream towards the top edge of the chip from its starting point at the Row (N+5) to the ending point at the Row (N+1). In another embodiment, the V4 line can be driven downstream towards the bottom edge of the chip or is a bi-directional line.

Please replace the paragraph that starts on page 12, line 6, with the following amended paragraph:

The V4 line 750 can be driven by a LAB output, a H4 line, a H24 line, a V16 line or the V4 line. In Fig. 9, the inputs of the multiplexer 759 are connected to a H4 line, a V4 line, a H4 line [[531]] 725, an output from the LAB 733, or an output from the LAB 734.